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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/008,515	11/08/2001	Jay B. Reimer	TT-30113	5418

TITLE OF INVENTION: MULTICORE DSP DEVICE HAVING SHARED PROGRAM MEMORY WITH CONDITIONAL WRITE PROTECTION

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a. Applicant claims SM	from status indicated above)  IALL ENTITY status. See 3 requested to apply the ireduced bit and irequired) with the control of the united Signers.	7 CFR 1.27.	b. Applica	ant is no longer claiming SMAL  y) or to re-apply any previously other than the applicant; a regist	I ENTITY croppe See 27 CV	0.1.07(.)(0)
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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

TI-30113

Jay B. Reimer, et al.

Art Unit: 2188

Serial No.:

10/008,515

Examiner: Jasmine Song

Filed:

October 30, 2001

Conf. No.: 5418

For:

Multicore DSP Device Having Shared Program Memory With Conditional Write

Protection

## **CERTIFICATION OF FACSIMILE TRANSMISSION**

I hereby certify that the following papers are being transmitted by facsimile to the U.S. Patent and Trademark Office at 703-746-4000 on the date shown below:

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NAME OF INVENTOR(S):		RECEIPT DATE & SERIAL NO.:		
Jay B.	Reimer, et al.	Serial No.: 10/008,515		
TITLE OF INVENTION: Multicore DSP Device Havir	- Charal Days	Filing Date: October 30, 2001		
Memory With Conditional W	rite Protection			
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TI-30113 20-0668				
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